

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )  
Shunpei YAMAMOTO, et al. )  
Serial No. 08/692,227 ) Art Unit: 1104  
Filed: August 7, 1996 ) Examiner: C. Chaudhari  
For: LASER PROCESSING METHOD, )  
METHOD FOR FORMING A FLASH MEMORY, )  
INSULATED GATE SEMICONDUCTOR )  
DEVICE AND METHOD FOR FORMING )  
THE SAME ) Date: April 22, 1997

INFORMATION DISCLOSURE STATEMENT

Honorable Assistant Commissioner for Patents  
Washington, D.C. 20231

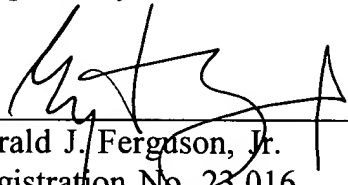
Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the references listed on the attached Form PTO-1449 be made of record in the above-identified application.

The references listed on the attached Form PTO-1449 were cited in parent application Serial No. 08/692,227 filed August 7, 1996, and the predecessor

applications including Serial No 08/335,652 filed December 14, 1994 and Serial No. 08/142,048 filed October 28, 1993.

Respectfully submitted,



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